

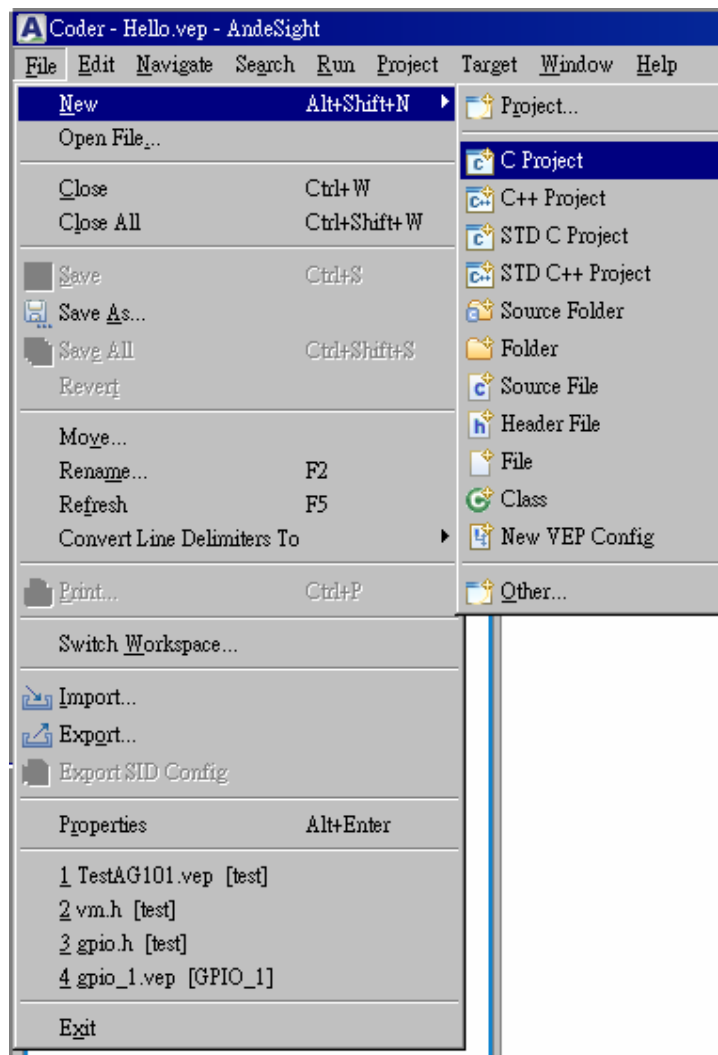
Lab1-2_AndeSight_GPIO

實驗目的:

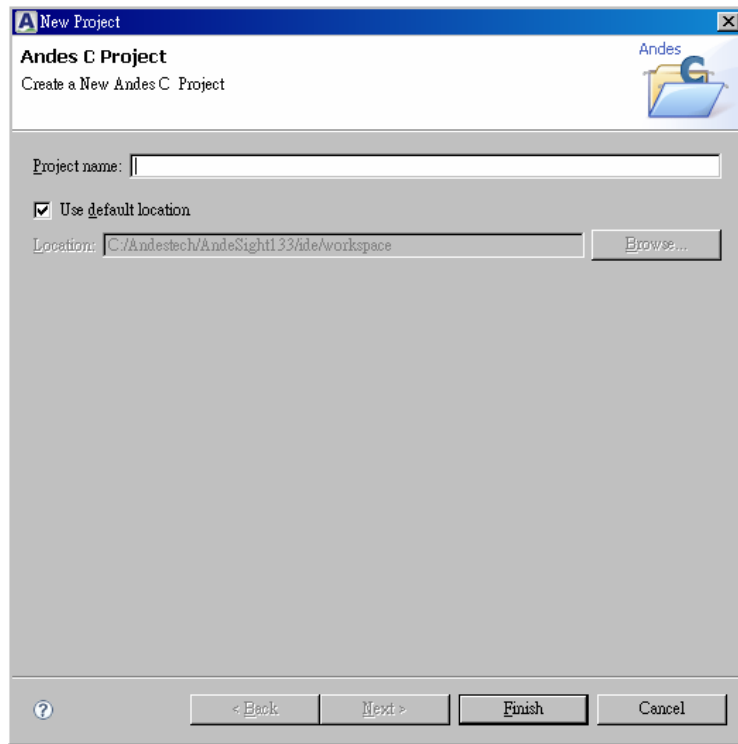
熟悉 Andesight 的操作與了解 Andesight 上 GPIO 的設定與 Memory mapping。

實驗步驟:

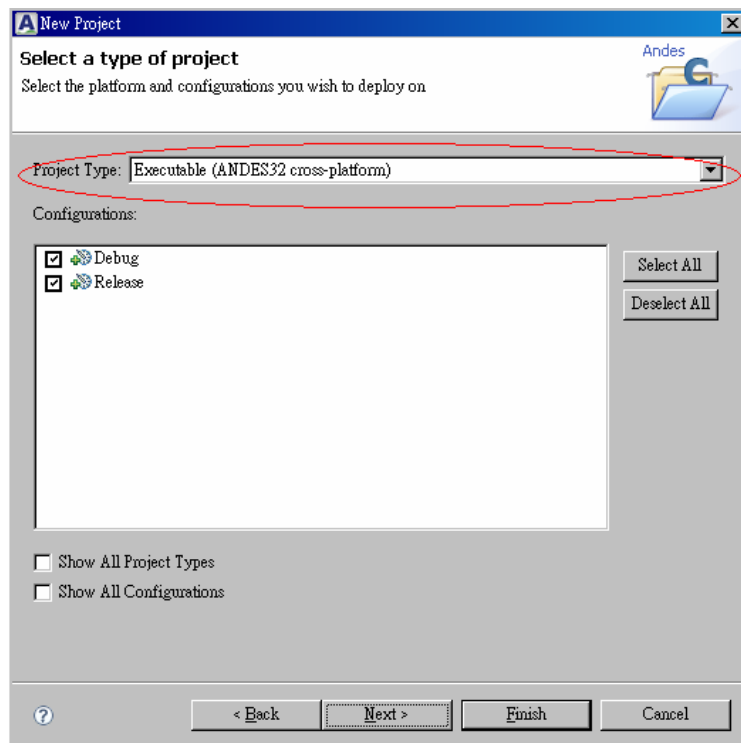
1:File->New->C Project



2:鍵入 Project name



3: Select a type of project



4. Choose Toolchain

New Project

Choose Toolchain

Choose an appropriate toolchain to build executables for your target system

Target type selection
Target Type Selection ☒ CPU ☐ Architecture

Target type
CPU Family ☐ n9 ☐ n10 ☒ n12
Architecture ☒ v0 ☐ v1 ☐ v1j ☐ v2 ☐ v2j

Endian
Endian Type ☒ little endian ☐ big endian

Default linkage
Default library ☐ glibc ☐ uclibc ☒ newlib

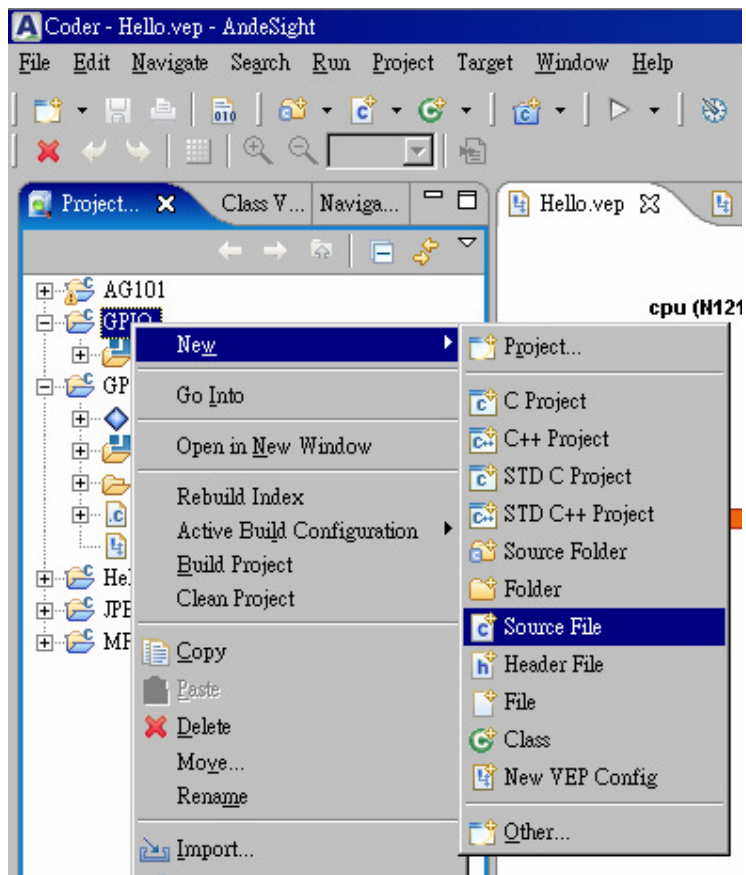
nds32le-elf-V0
nds32le-elf-V1

Properties

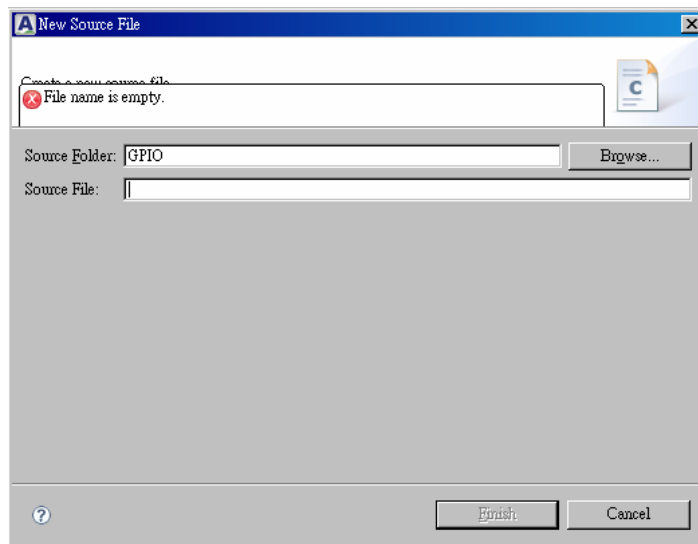
< Back Next > Finish Cancel

Key	Value
LIB_C_UCLIBC_STATIC	false
NDS32_TARGET	nds32le-elf
LIB_C_NEWLIB_STATIC	true
LIB_C_UCLIBC_SHARED	false
LIB_CPP_NEWLIB_SHARED	false
LIB_C_GLIBC_SHARED	false
LIB_C_NEWLIB_SHARED	false
NDS32_CORE	n12
LIB_CPP_UCLIBC_STATIC	false
LIB_CPP_NEWLIB_STATIC	true
NDS32_ENDIAN	little
NDS32_OS	
NDS32_KERNEL_VER	
LIB_CPP_UCLIBC_SHARED	false
LIB_C_GLIBC_STATIC	false
NDS32_ARCH	v0
LIB_CPP_GLIBC_STATIC	false
LIB_CPP_GLIBC_SHARED	false
NDS32_LIST_CPU	N1213-43U1H
NDS32_CPU	n1213_43ulh

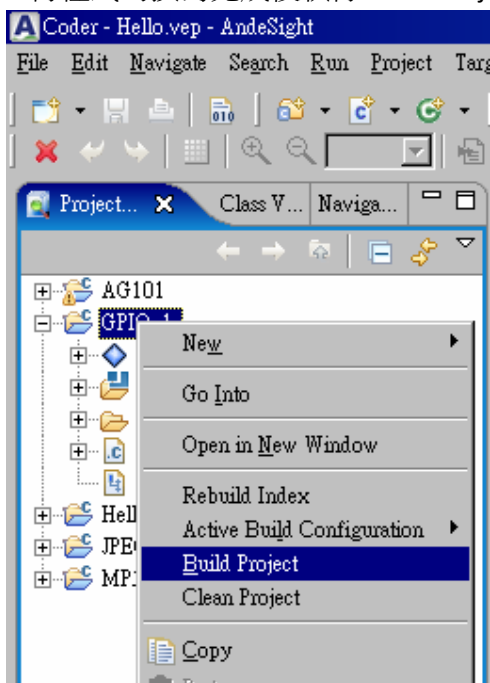
5.在 Project name 按右鍵 New->Source file



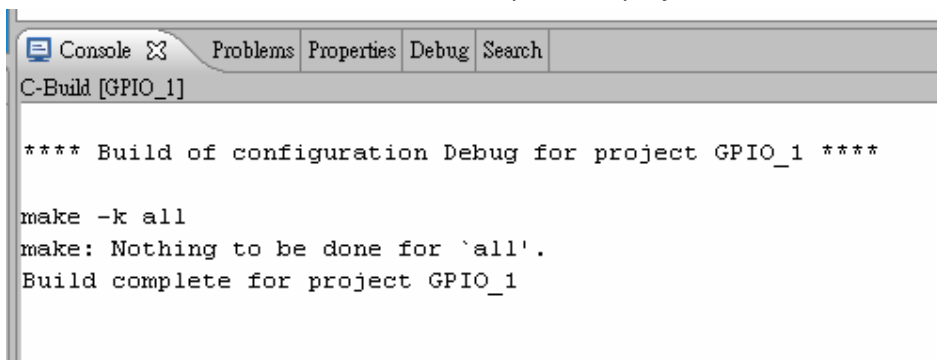
6. Source file name: *****.c



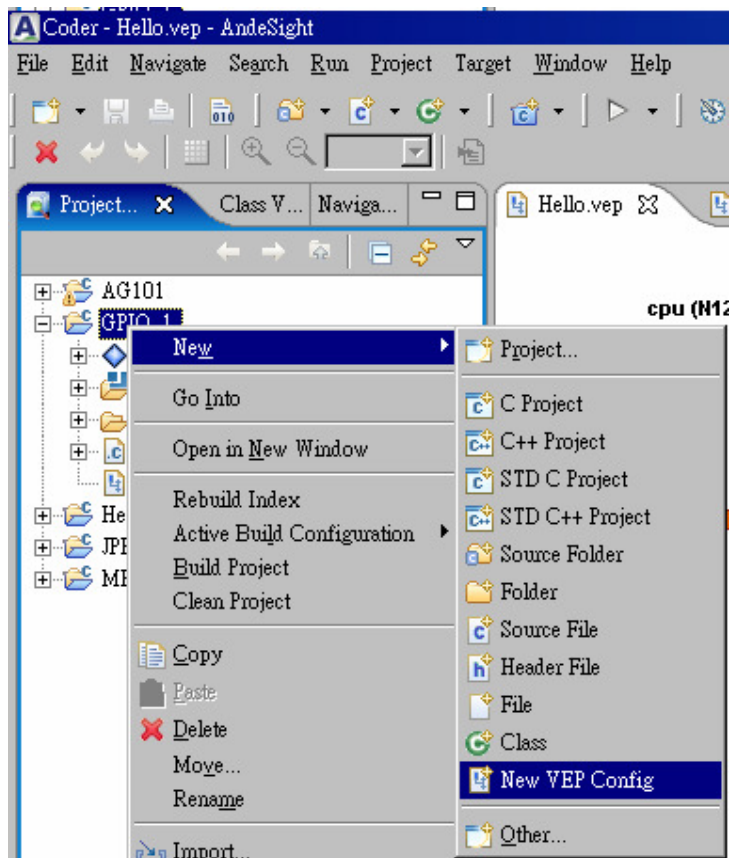
7.待程式碼撰寫完成後執行 Build Project



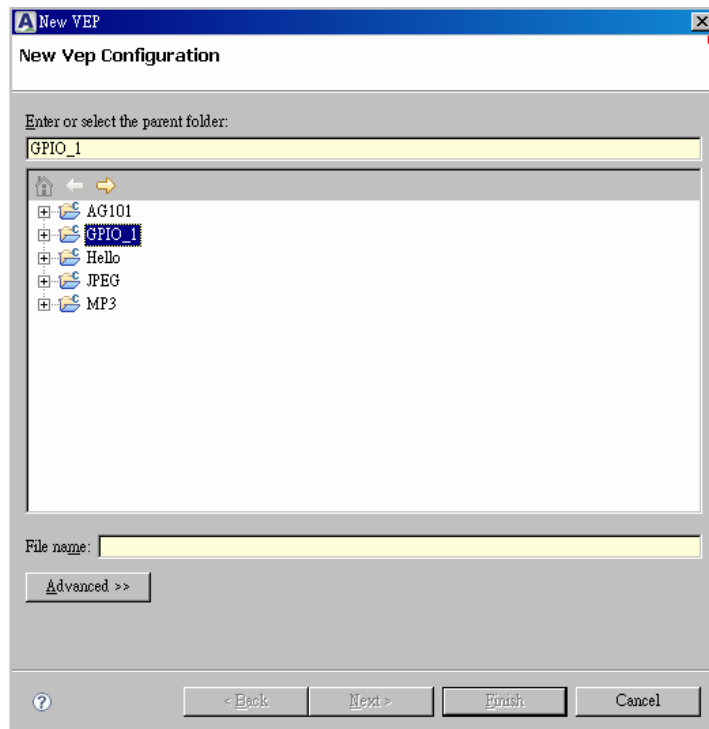
8.注意 Console 的訊息，出現 Build complete for project，才算完成。



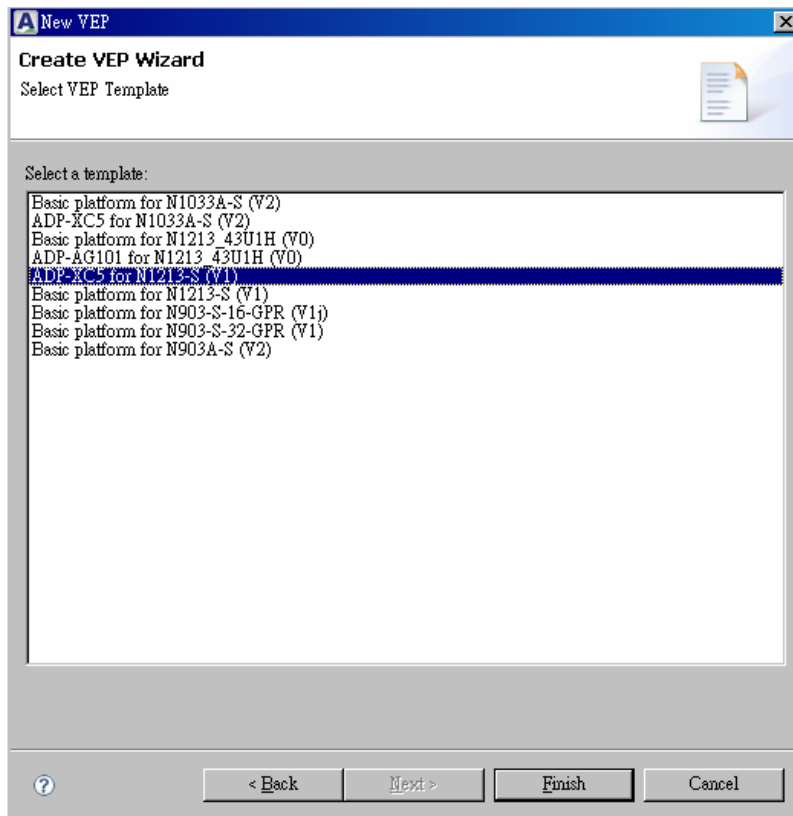
9.產生 VEP，在 Project name 按右鍵 New->New VEP Config



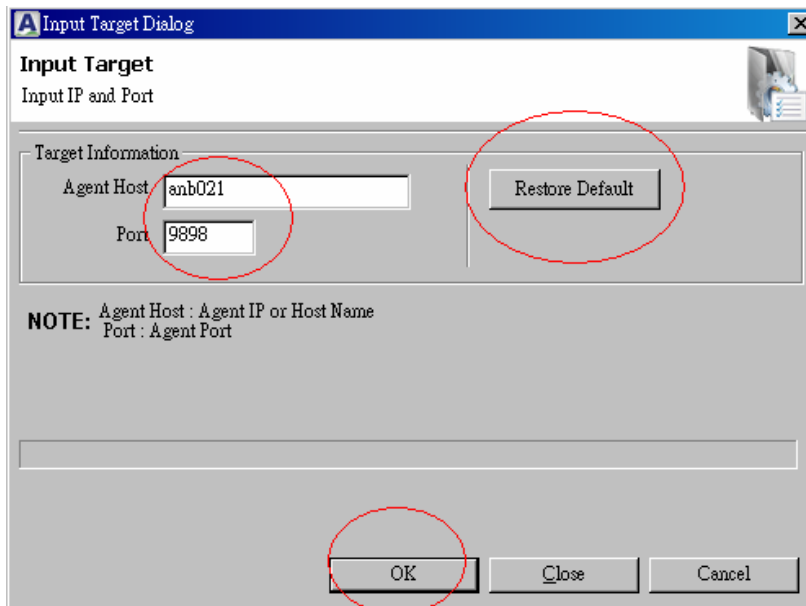
10. New VEP Configuration->File name: *****.vep



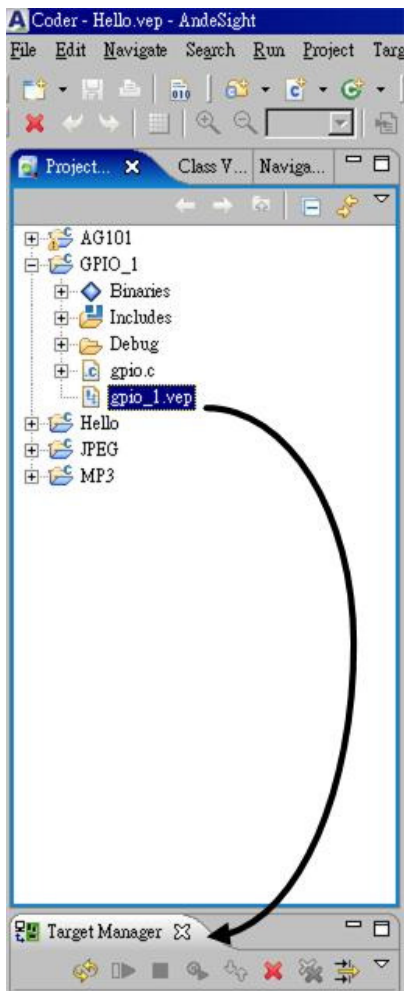
11. Create a VEP Wizard



12. 連接 Target->Input Target

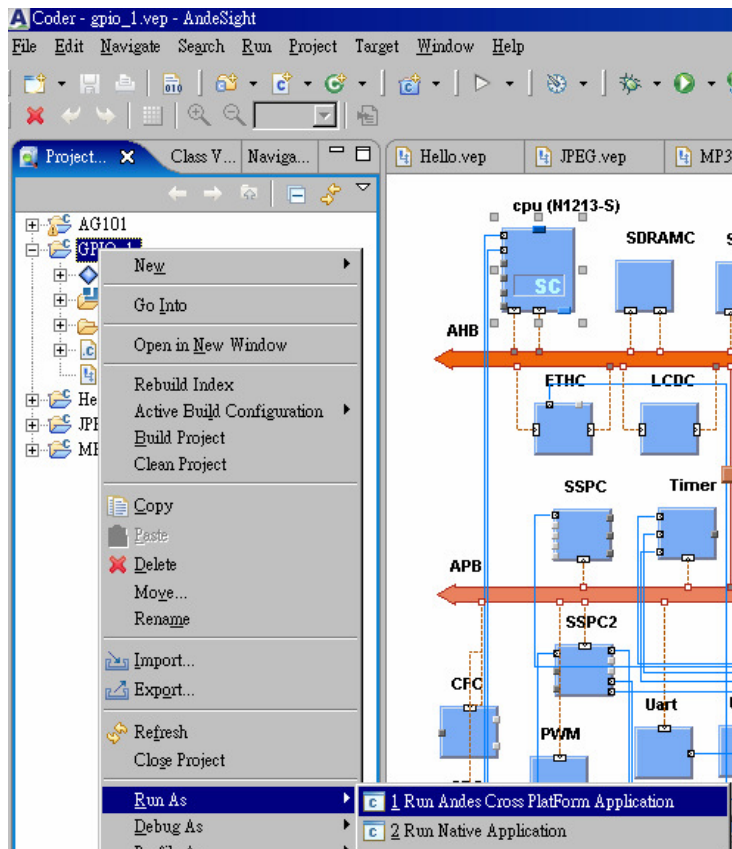


13. 將*****.vep 利用滑鼠左鍵拖曳到下方的 Target Manager



13. 檢查 Virtual I/O (如有用到 Virtual I/O)，如果更改設定，需要將 VEP 重新 save。

14. 在 Project name 按右鍵 Run->Run As->Run Andes Cross PlatForm Application



14. 檢查 Virtual I/O->GPIO

15. Source Code (如附件)